



EqcoLogic launches transceivers for camera interfaces using CoaXPress standard

Stuttgart, 3rd November, 2009; EqcoLogic, a Belgian fab-less semiconductor vendor has launched new transceiver products that support the newly announced CoaXPress standard for high performance camera systems.

EqcoLogic is one of the founder members of the CoaXPress Consortium that is developing a revolutionary new high speed digital camera interface solution. Using the EqcoLogic transceivers, CoaXPress is capable of sending high speed video, control and power over a single conventional coaxial cable. Communication over the cable is “asymmetric” – up to 6.25Gbit/s from camera to frame grabber or server and up to 20Mbit/s the other way for control data going to the camera. The system also supports up to 13Watts of power to be transmitted to the camera, eliminating the need for an additional power supply at the camera.

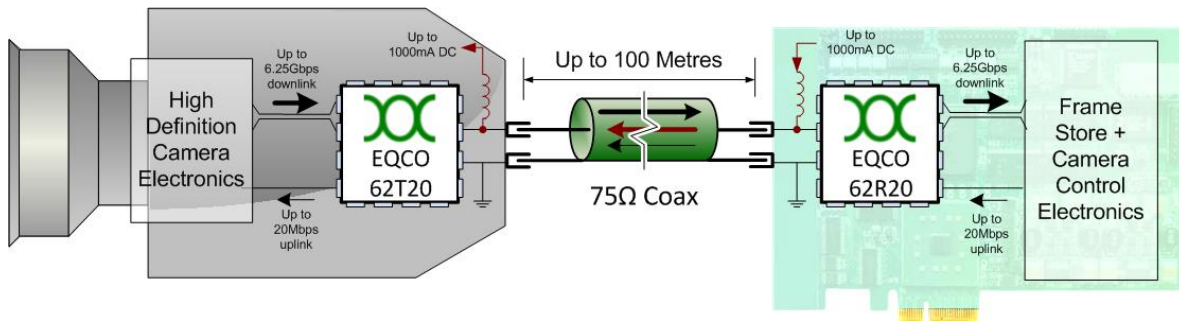


Illustration of CoaXPress Link using EqcoLogic transceivers

EqcoLogic devices are the EQCO62T20 for high speed transmission at the camera end and the EQCO62R20 for the high speed receive function at the frame grabber. The receiver incorporates a high performance adaptive Equalizer that enables long cables – up to 100m. These devices assure cost-effective solutions can be achieved through continued use of conventional cables and connectors.

The EQCO62T20 and EQCO62R20 are shipping now in sample quantities and will be in mass production in the first half of 2010.

Further information from EqcoLogic:

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About EqcoLogic

EqcoLogic is a Belgian company, spun-out of the Vrije Universiteit Brussel.

Formed in 2005, the company is commercialising innovative Equalizer and transceiver technology.

EqcoLogic is a fab-less semiconductor vendor but the company also sub-licenses the core technology and product designs in certain markets. Along with exceptional signal recovery, EqcoLogic chips offer good operational tolerances and low power consumption. Designed for standard CMOS processes, the circuits can also be integrated into other communications chips.